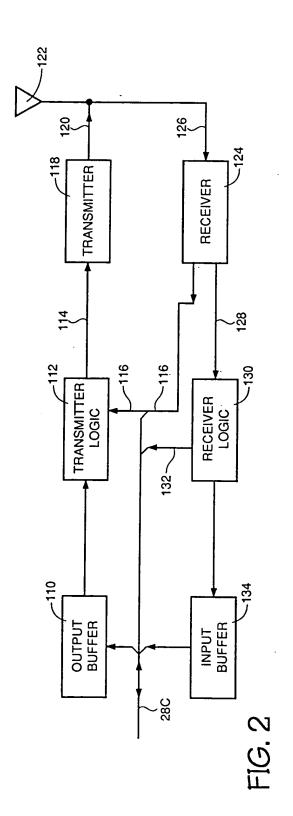
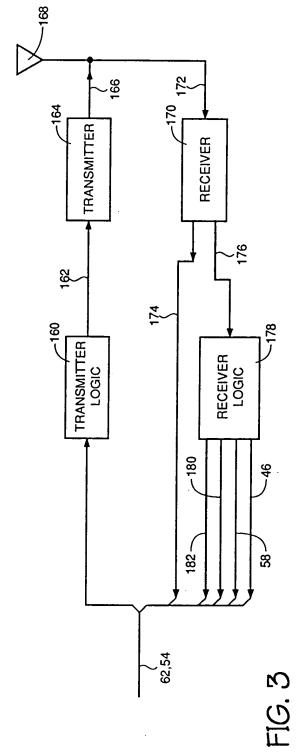


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- (11) (12) (13)



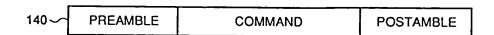


FIG. 4

COMMANDS								
OPCODE (HEXADECIMAL)	MNEMONIC	MNEMONIC DESCRIPTION						
08	ID IDENTIFY							
A9	IDG	IDENTIFY AND GENERATE ARBITRATION NUMBER	142					
EA	IDC	IDENTIFY AND CLEAR LOCKED BIT 88	142					
4B	IDCG	IDENTIFY, CLEAR LOCKED BIT 88, AND GENERATE ARBITRATION NUMBER	142					
8E	RD	READ DATA AND SET LOCKED BIT 88	144					
2F	2F WD WRITE DATA AND SET LOCKED BIT							

FIG. 5

142~	OPCODE	MASK	BRANCH	LOCAL ID		
144~	OPCODE	ARBITRATIO	ON NUMBER	LOCAL ID		
146~	OPCODE	ARBITRATIO	ON NUMBER	LOCAL ID	TAG	DATA

FIG. 6

1	90~		
	PREAMBLE	RESPONSE	POSTAMBLE

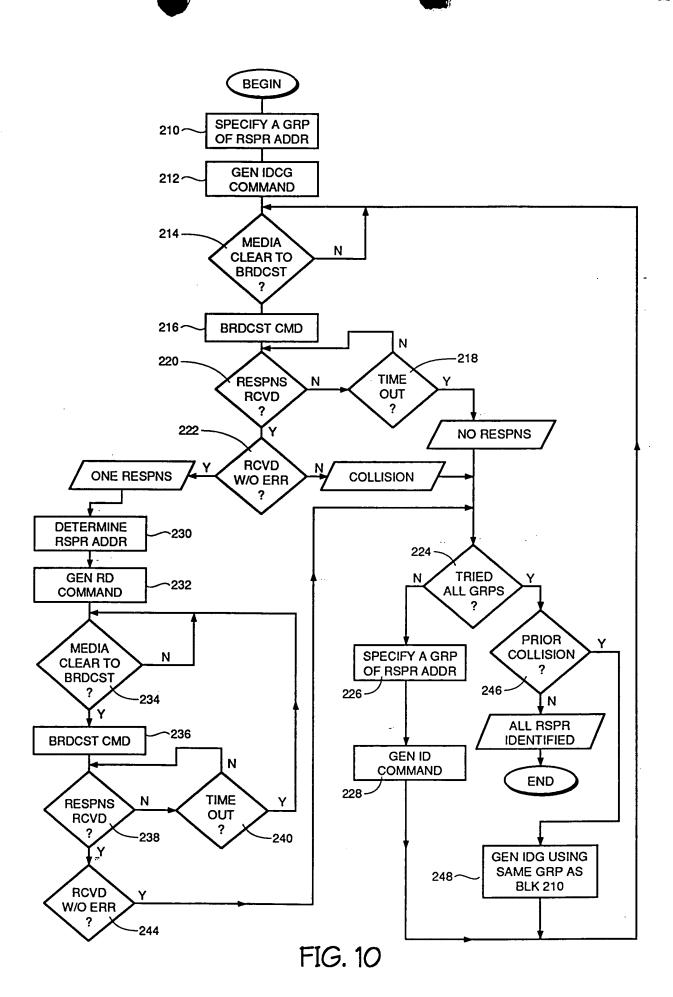
FIG. 7

RESPONSES							
MNEMONIC	FORMAT						
IDR	RESPONSE TO ID, IDG, IDC, AND IDCG COMMANDS	192					
RDR	RESPONSE TO RD COMMAND	194					
WDR	RESPONSE TO WR COMMAND	196					

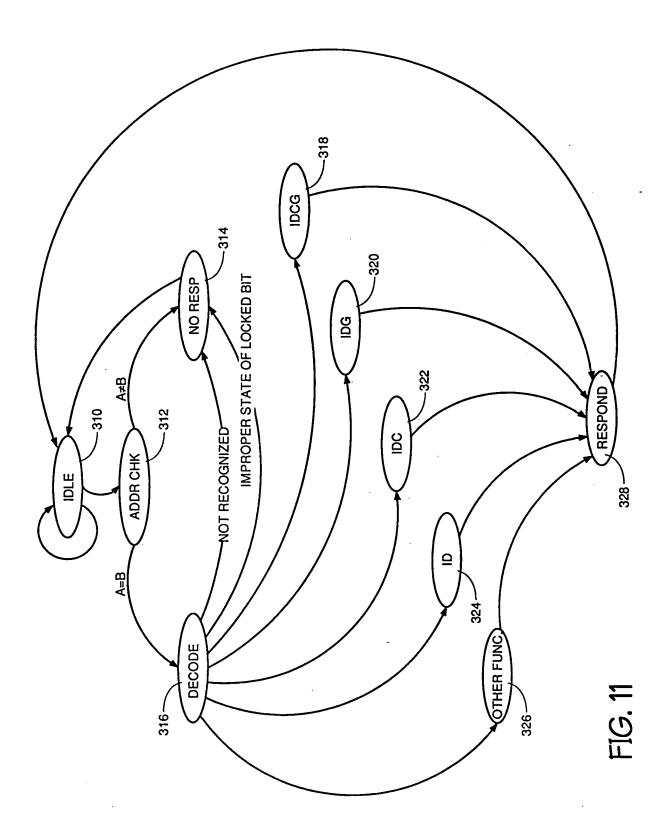
FIG. 8

192		_						
REVISION	LOCAL ID	ARBITRATIO	N NUMBER	INVERTED	ARBITRATION NUMBER			
194								
ARBITRATION NUMBER		LOCAL ID	TAG	DATA				
196				•	_			
ARBITRATION NUMBER		LOCAL ID STATUS						

FIG. 9









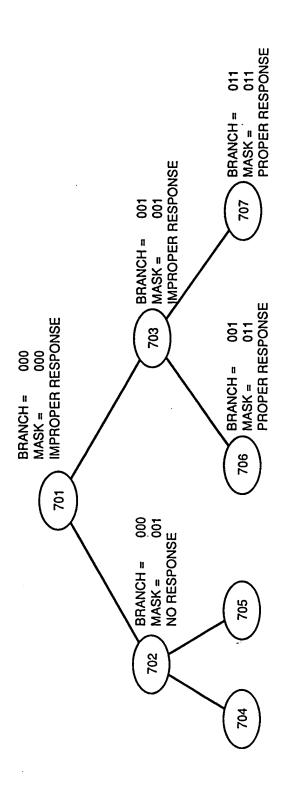


FIG. 12

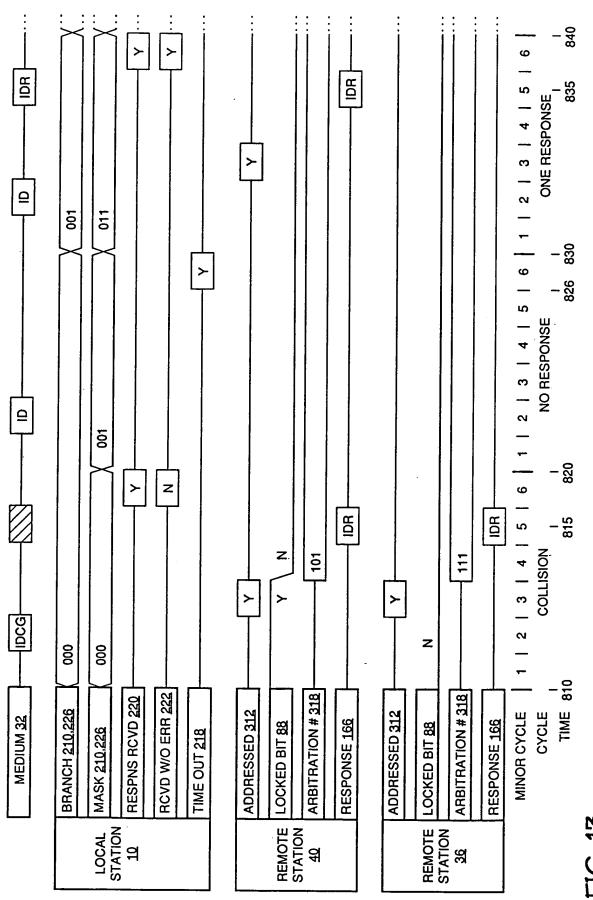


FIG. 13

RDR ID	011	\hat{\lambda}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			λ /		RDR		<u></u>			IDR	2   3   4   5   6   1   2   3   4   5   6	SET LOCKED BIT ONE RESPONSE	845 850 855 860	
MEDIUM 32 RD	BRANCH <u>210,226</u> 101 MASK <u>210,226</u> 111	RESPNS RCVD 220	RCVD W/O ERR 222	TIME OUT 218	ADDRESSED 312	LOCKED BIT 88	ARBITRATION # 318 101	RESPONSE 166	-	ADDRESSED 312	LOCKED BIT 88 N	ARBITRATION # 318 111	RESPONSE 166	MINOR CYCLE 1		TIME 840	
	LOCAL STATION 10				REMOTE	40 80 80 80 80 80 80 80 80 80 80 80 80 80				REMOTE	98 98					i	

FIG. 14

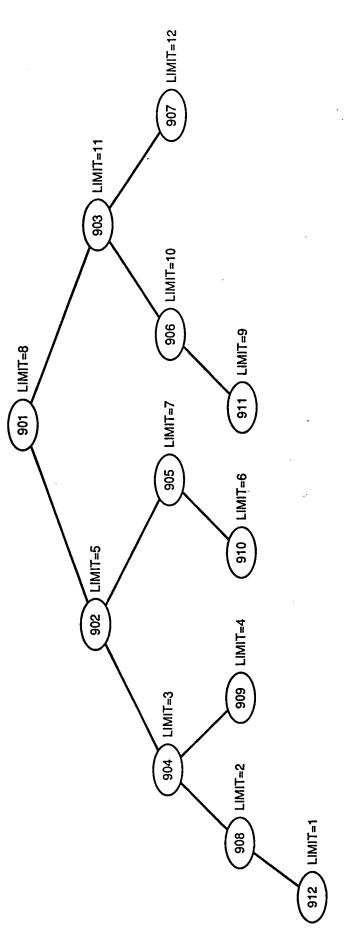


FIG. 15